

VLSI & EMBEDDED SYSTEM

5th Sem Diploma (ECE)

1. Answer any Three (3*2 Marks)
 - (a) Write the basic steps in Fabrication Process flow.
 - (b) Draw diagram of CMOS.
 - (c) What is the number of address line required for 256*256 memory.
 - (d) Draw the diagram of CMOS D latch

2. Answer any Two (2*3.5 Marks)
 - (a) Explain Y chart.
 - (b) Explain level-3 model for MOS Transistor.
 - (c) Describe basic principle of Dynamic Pass Transistor Circuits.

3. Answer any One (1*7 Marks)
 - (a) Explain Fabrication process of nMOS Transistor.
 - (b) Explain MOSFET V-I characteristics.
 - (c) Describe Clocked SR latch.